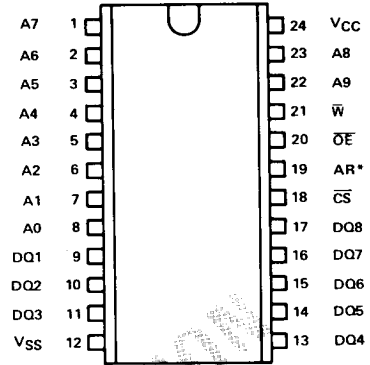


- 1K X 8 Organization
- Single +5 V Supply
- Fully Static Operation (No Clocks, No Refresh)
- JEDEC Standard Pinout
- 24-Pin 600 (15.2 mm) Mil Package Configuration
- Plug-in Compatible with 8K 5 V EPROMs
- 8-Bit Output for Use in Microprocessor-Based Systems
- Max Access . . . 450 ns
- Tri-State Outputs with  $\overline{CS}$  for OR-Ties
- $\overline{OE}$  Eliminates Need for External Bus Buffers
- Common I/O Capability
- All Inputs and Outputs Fully TTL Compatible
- Fanout to Series 74, Series 74S, or Series 74LS TTL Loads
- N-Channel Silicon-Gate Technology
- Power Dissipation Under 475 mW Max
- Guaranteed dc Noise Immunity of 400 mV with Standard TTL Loads

TMS 4008  
24-PIN PLASTIC  
DUAL-IN-LINE PACKAGE  
(TOP VIEW)



\* AR tied to  $V_{IL}$  if TMS 4008-NL0  
 $V_{IH}$  if TMS 4008-NL1

PIN NOMENCLATURE	
A0-A9	Addresses
AR	Array Select
DQ1-DQ8	Data In/Data Out
$\overline{CS}$	Chip Select
$\overline{OE}$	Output Enable
$\overline{W}$	Write Enable
VSS	Ground
VCC	+5 V Supply

**description**

The TMS 4008 static random-access memory is organized as 1024 words of 8 bits each. Fabricated using proven N-channel, silicon-gate MOS technology, the TMS 4008 operates at high speeds and draws less power per bit than 4K static RAMs. It is fully compatible with Series 74, 74S, or 74LS TTL. Its static design means that no refresh clocking circuitry is needed and timing requirements are simplified. Access time is equal to cycle time. A chip select control is provided for controlling the flow of data-in and data-out and an output enable function is included in order to eliminate the need for external bus buffers.

Of special importance is that the TMS 4008 static RAM has the same standardized pinout as TI's compatible EPROM family. Minimal, if any modifications are needed. This allows the microprocessor system designer complete flexibility in partitioning his memory board between read/write and non-volatile storage.

The TMS 4008 is offered in the plastic (NL suffix) 24-pin dual-in-line package designed for insertion in mounting hole rows on 600-mil (15.2 mm) centers. It is guaranteed for operation from 0°C to 70°C.

COMPLETE INFORMATION MAY BE OBTAINED FROM YOUR LOCAL TI SALES OFFICE OR AUTHORIZED DISTRIBUTOR LISTED NEAR THE FRONT OF THIS DATA BOOK.

**ADVANCE INFORMATION**

90 This document contains information on a new product. Specifications are subject to change without notice.

**TEXAS INSTRUMENTS**  
INCORPORATED

POST OFFICE BOX 225012 • DALLAS, TEXAS 75265

Copyright © 1979 by Texas Instruments